AMENDMENTS TO THE DRAWINGS:

In response to the Draftsperson's objections to the informal drawings, attached are formalized drawings of Figs. 1-4c as originally filed. No new matter has been added.

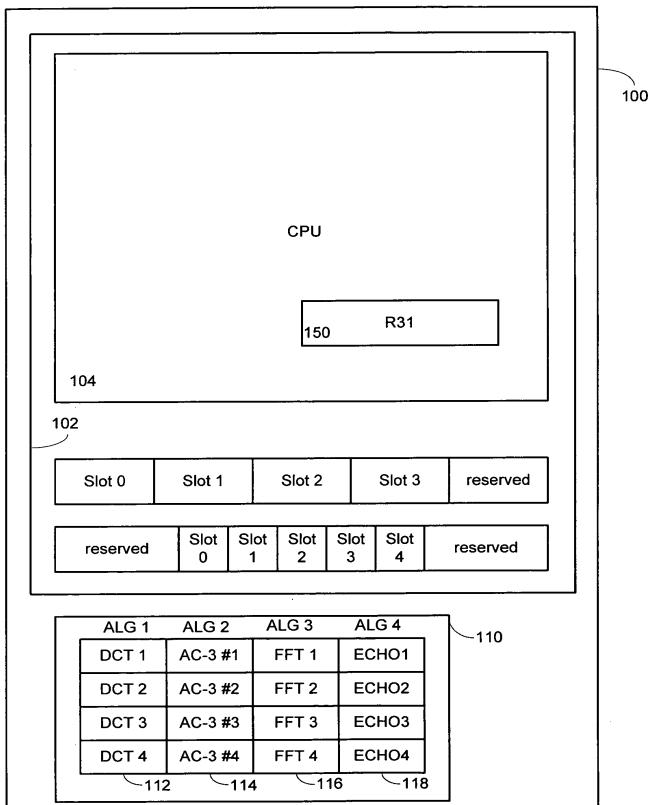
Attachment: 8 Sheets of Formal Drawings

APPENDIX



REPLACEMENT SHEET

Fig. 1





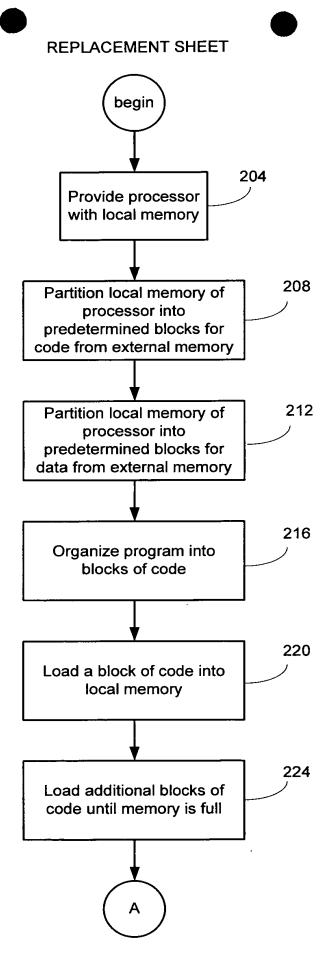


Fig. 2a



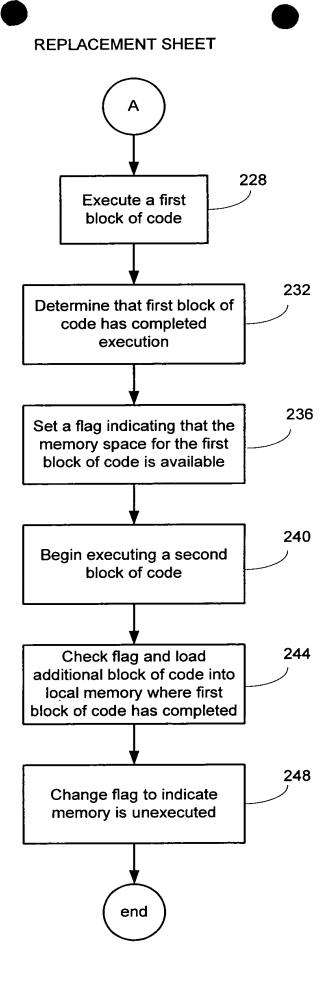


Fig. 2b



REPLACEMENT SHEET begin 304 Provide local memory coupled to a processor 308 Store a plurality of algorithm codes in external memory 312 Divide local memory into predefined memory blocks 316 Configure a section of local memory to serve as flags 320 Put each algorithm M into a queue 324 Load first block of algorithm code into local memory 328 Set a flag corresponding with the block of memory 332 Load additional blocks of code

Fig. 3a

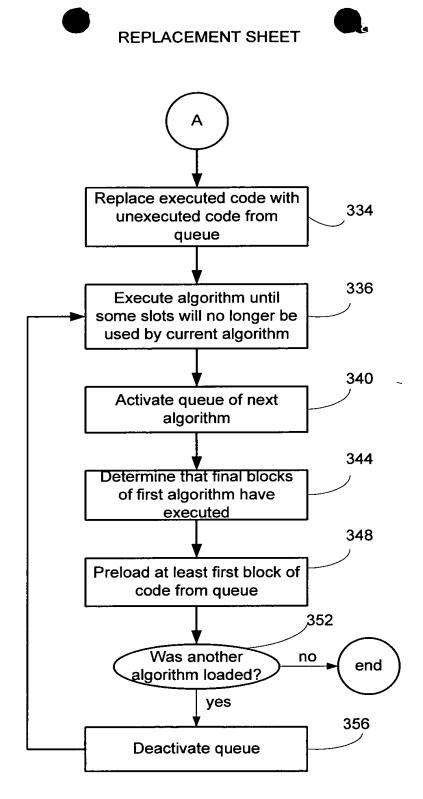


Fig. 3b

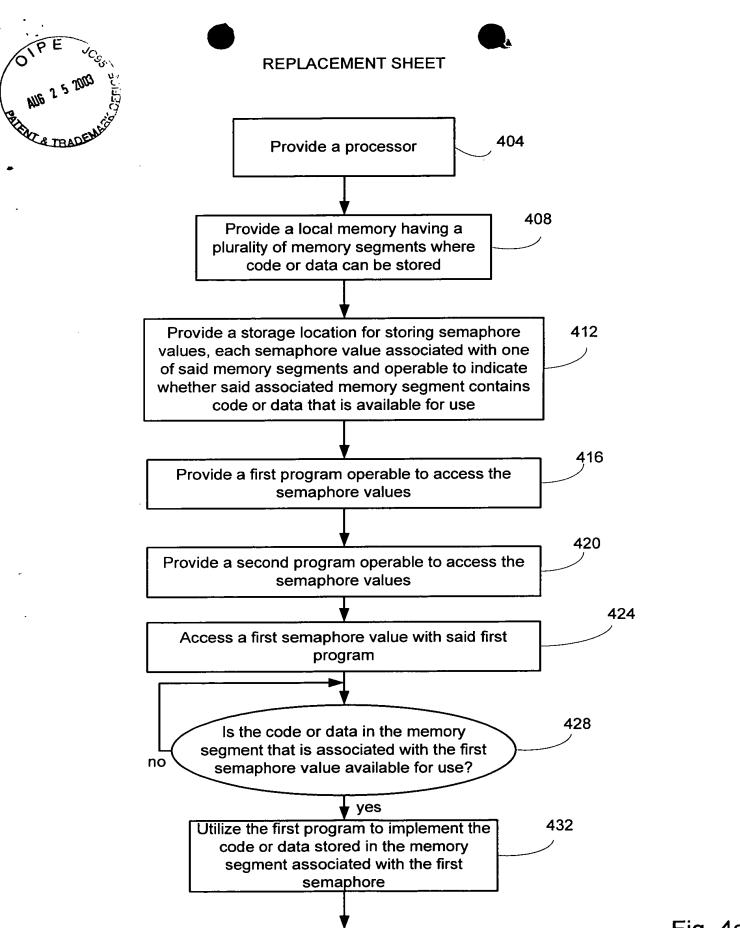
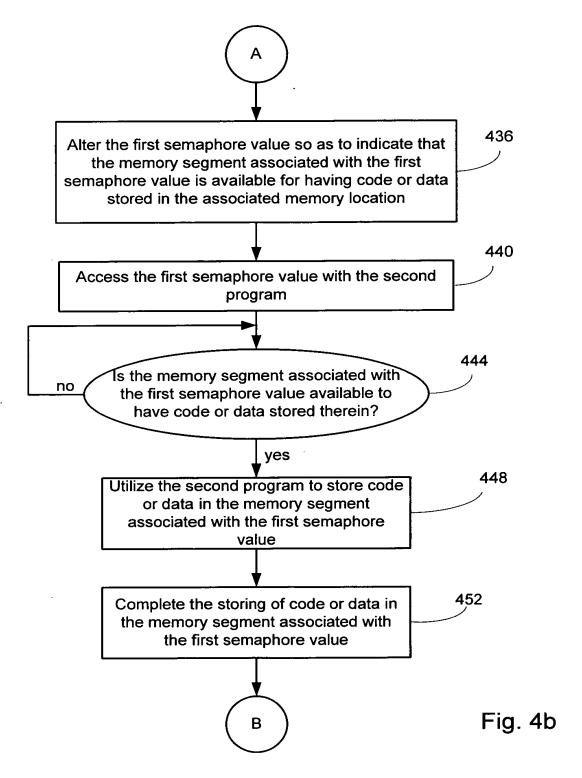


Fig. 4a

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REPLACEMENT SHEET



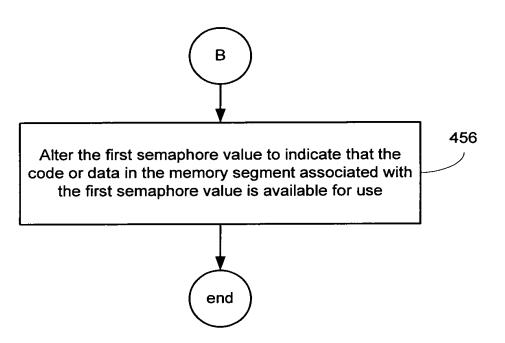


Fig. 4c